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AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

**Claims 1-2. (Canceled).**

**Claim 3. (Canceled).**

4. (Currently Amended) The semiconductor integrated circuit comprising:  
an input pad; and  
an input circuit for taking in external signals through the input pad,  
wherein the input circuit includes a dynamic terminator resistor circuit that can  
be adjusted so that an input impedance during an input signal transition is lower than  
an input impedance on other occasions than at times other than during the input  
signal transition,

wherein the dynamic terminator resistor circuit comprises:  
a first logic circuit for inverting the logic of a signal transmitted through the  
input pad;  
a second logic circuit for inverting the logic of the output signal of the first logic  
circuit; and  
a resistor-resistor that can connect the input terminal of the first logic circuit  
with the output terminal of the second logic circuit.

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5. (Currently Amended) The semiconductor integrated circuit comprising:  
an input pad; and  
an input circuit for taking in external signals through the input pad,  
wherein the input circuit includes a dynamic terminator resistor circuit that can  
be adjusted so that an input impedance during an input signal transition is lower than  
an input impedance on other occasions than at times other than during the input  
signal transition,  
wherein the dynamic terminator resistor circuit comprises:  
a first logic circuit for inverting the logic of a signal transmitted through the  
input pad;  
a second logic circuit for inverting the logic of the output signal of the first logic  
circuit;  
a resistor-resistor that can connect the input terminal of the first logic circuit  
with the output terminal of the second logic circuit; and  
a third logic circuit for transmitting the output signal of the first logic circuit to  
an internal circuit.

6. (Currently Amended) The semiconductor integrated circuit according to  
Claim 4 or Claim 5, including:  
a switch circuit capable of controlling the involvement of the resistor-resistor  
with circuit operation.

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7. (Currently Amended) The semiconductor integrated circuit according to  
**Claim 4 or Claim 5,**

wherein the dynamic terminator resistor circuit comprises:

a first logic circuit for inverting the logic of a signal transmitted through the input pad;

a second logic circuit for inverting the logic of the output signal of the first logic circuit;

a plurality of ~~resistors~~ resistors that can connect the input terminal of the first logic circuit with the output terminal of the second logic circuit; and

a switch circuit for selectively getting a plurality of the ~~resistors~~ resistors involved with circuit operation.

**Claim 8. (Canceled).**

9. (Currently Amended) The semiconductor integrated circuit according to  
**Claim 8 comprising:**

an internal circuit; and

an output circuit that can externally output an output signal of the internal circuit.

wherein the output circuit comprises:

a first output circuit that can drive an external load based on the output signal of the internal circuit during a first half of transition of a signal to be outputted; and

a second output circuit whose driving force is set lower than that of the first

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output circuit and which can drive the external load, said semiconductor integrated circuit further including:

a level monitor circuit for selectively getting the first output control circuit or the second output circuit involved with circuit operation according to the voltage level of the external load.

10. (Currently Amended) The semiconductor integrated circuit according to Claim 8 comprising:

an internal circuit; and

an output circuit that can externally output an output signal of the internal circuit,

wherein the output circuit comprises:

a first output circuit that can drive an external load based on the output signal of the internal circuit during a first half of transition of a signal to be outputted; and

a second output circuit whose driving force is set lower than that of the first output circuit and which can drive the external load,

wherein the second output circuit includes a series connection circuit of an n-channel transistor disposed on a higher-potential power supply side and a p-channel transistor disposed on a lower-potential power supply side, and a series connection node between the n-channel transistor and the p-channel transistor is coupled to the output node of the first output circuit.

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11. (Currently Amended) A semiconductor integrated circuit comprising:
- an input portion so set that an input impedance during an input signal transition is lower than an input impedance ~~on other occasions than~~ at times other than during the input signal transition; and
- an output portion so set that a driving force during a second half of signal transition is lower than a driving force during a first half of transition,
- wherein the output portion comprises:
- a first output circuit that can drive an external load based on the output signal of the internal circuit during the first half of transition of a signal to be outputted; and
- a second output circuit whose driving force is set lower than that of the first output circuit and which can drive the external load,
- wherein the second output circuit includes a series connection circuit of an n-channel transistor disposed on a higher-potential power supply side and a p-channel transistor disposed on a lower-potential power supply side, and
- wherein a series connection node between the n-channel transistor and the p-channel transistor is connected in common to the input/output pad together with the output node of the first output circuit, and the series connection circuit is also used as part of the input portion.